

```

mirror.mod.use.y = False
mirror.mod.use.z = False
elif operation == "MIRROR_X":
    mirror.mod.use.x = False
    mirror.mod.use.y = True
    mirror.mod.use.z = False
elif operation == "MIRROR_Z":
    mirror.mod.use.x = False
    mirror.mod.use.y = False
    mirror.mod.use.z = True
    mirror.mod.use.x = False

#selection of the use of back the deselect a mirror of the
mirror.ob.select.mod.use = True
modifier.ob.select = 1
bpy.context.scene.objects.active = modifier.ob
print("Selected the modifier ob")
#modifier ob is the one
main_modifier.ob.select = 1
#one obpy.context.scene.objects.active
#obpy.context.scene.objects.active = modifier.ob
#and please select exactly two objects, the last one gets the
OPERATOR.CANCEL = True

```

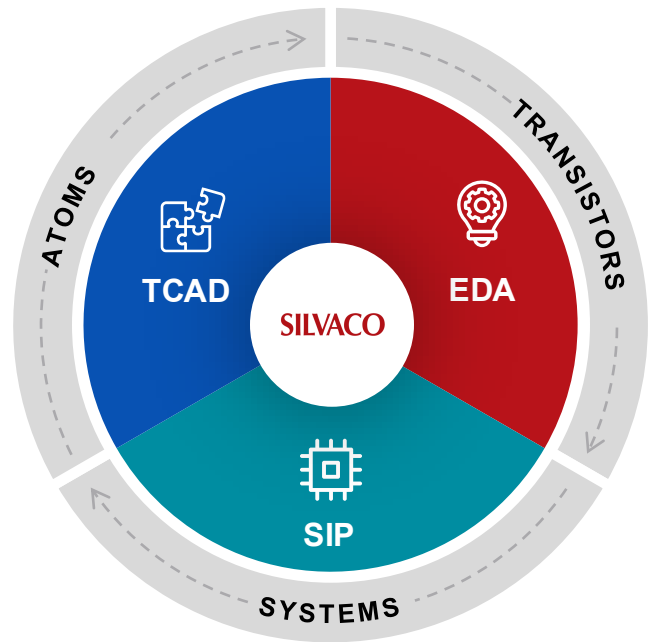
Enabling Semiconductor Design and Manufacturing Through AI, Digital Twin Modeling, Software Innovation and Automation

FAE – III

Silvaco at a Glance

Enabling semiconductor innovation from atoms to systems

Design, Simulate, Analyze, and Verify
from Concept to Complete Product Yield



Silvaco Leadership in Semiconductor Software Solutions

8 of 10⁽¹⁾

Largest Display
Companies Use
Silvaco's TCAD

4 of 10⁽⁴⁾

Largest SiC, GaN Power
Device Providers Use
Silvaco TCAD

6 of 10⁽³⁾

Largest Semiconductor
Companies Use
Silvaco's EDA

800+ ⁽²⁾

Customers

200+

University Customers

300+⁽⁵⁾

Employees

Global Presence

Headquarters &
Development Center
Silicon Valley, USA

- Sales Offices
- R&D Centers
- Sales/R&D Centers



Automotive



IoT



5G / 6G



Display



Memory



HPC



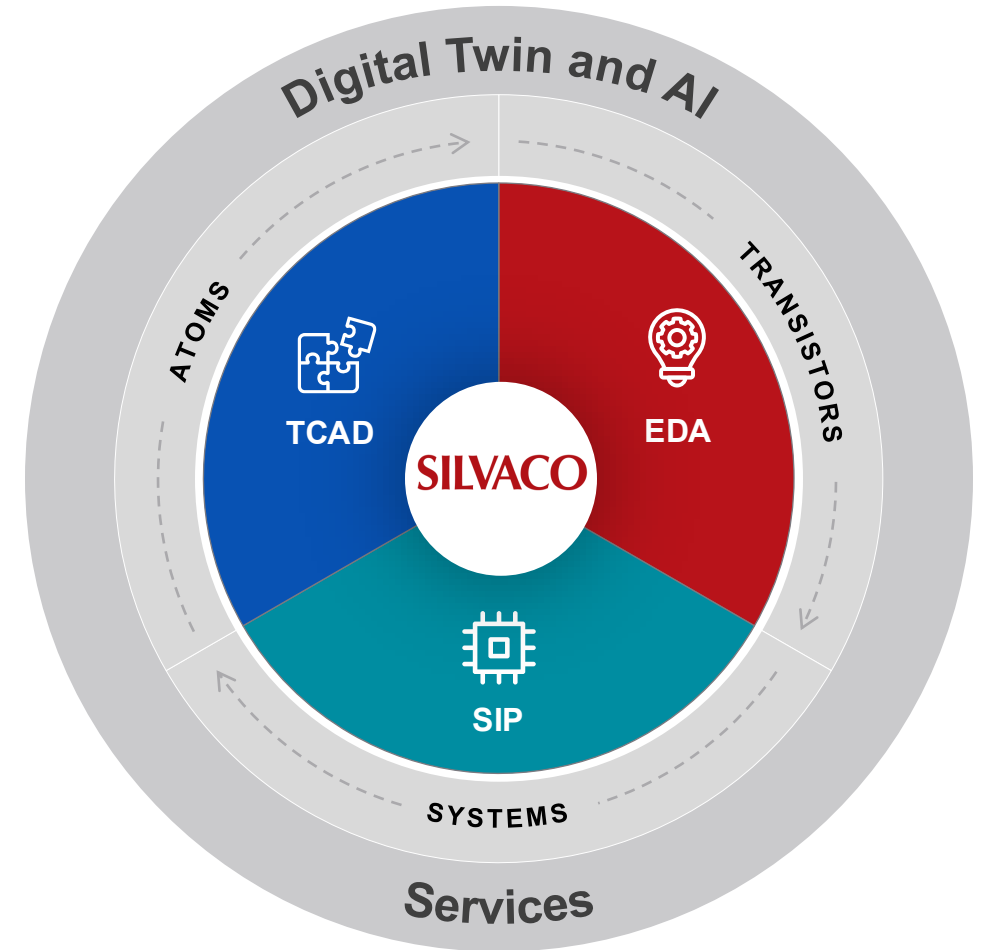
Power

SILVACO

- 1) Largest in terms of revenue from Q4'2023 to Q3,2024 according to Counterpoint Research
- 2) Data as of September 30, 2024
- 3) Largest in terms of revenue according to companiesmarketcap.com
- 4) 2024 Future Market Insight SiC and GaN Semiconductor Market Report
- 5) Data as of August 2025

Silvaco's Technology Portfolio

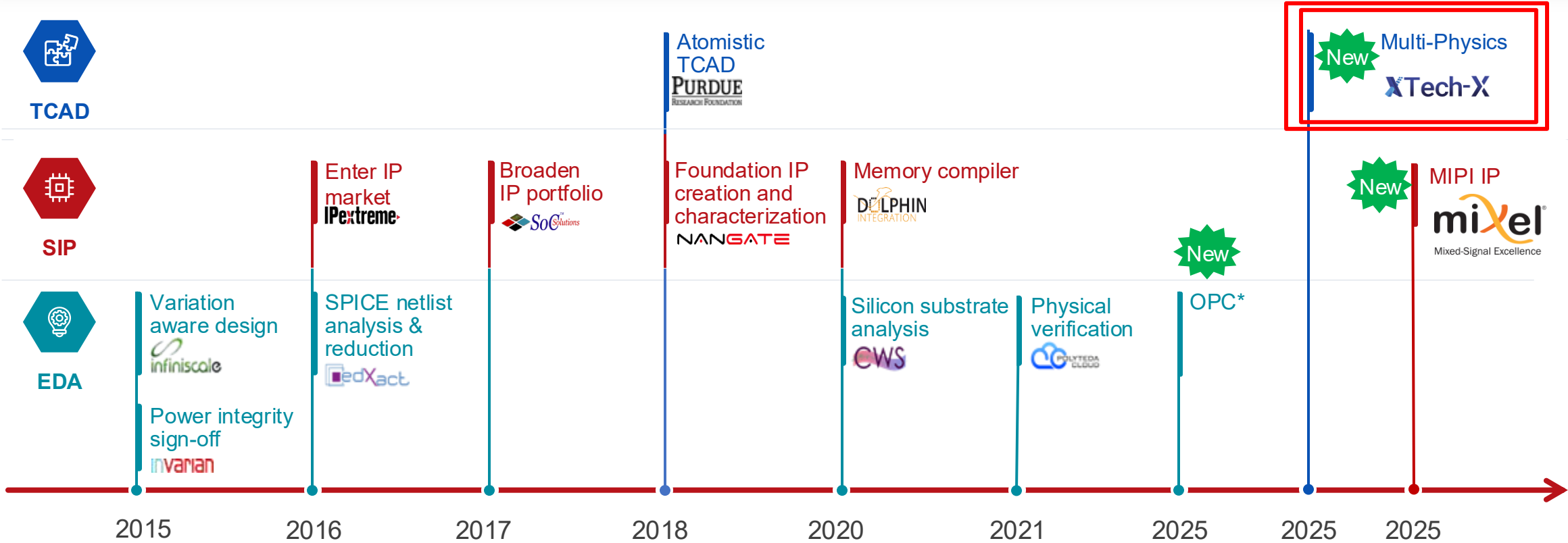
- **Leading Technology Computer Aided Design (TCAD) Solution**
 - Complete 2D/3D process, device, atomistic simulation
 - Power, advanced CMOS, Display and Optoelectronics
 - AI-Driven Fab Technology Co-Optimization (FTCO™)
- **Superior Integrated Circuit (IC) Analysis & Verification Tools**
 - Leading parasitic reduction and analysis
 - Advanced modeling utilizing Machine Learning
 - Variation analysis
 - Sign-off accepted physical verification tools
 - Analog/custom IC design
 - Complete flow from Schematic to Foundry (GDSII)
- **Silicon Packaging and Management**
 - Large portfolio: I3C, AMBA Fabric, MIPI, I/O, Memory, Std. Cell
 - Enterprise IP management products
- **Complete suite of Electronic Design Automation (EDA) tools**
 - Optimized with Fab Technology Co-Optimization (FTCO™); Digital Twin and AI processing



ISO 9001 Certified

Strategic Acquisitions Expanding Solutions Portfolio

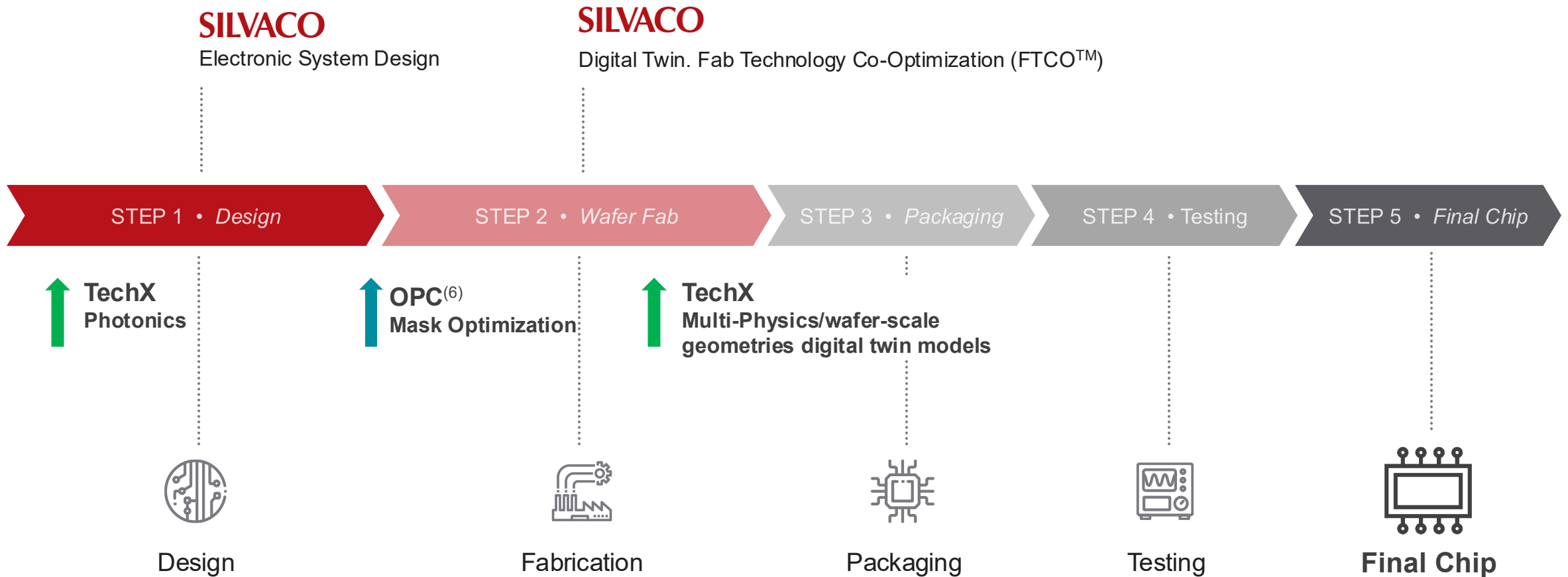
Silvaco Has Successfully Integrated 13 Acquisitions



*Acquired from Cadence Design Systems.

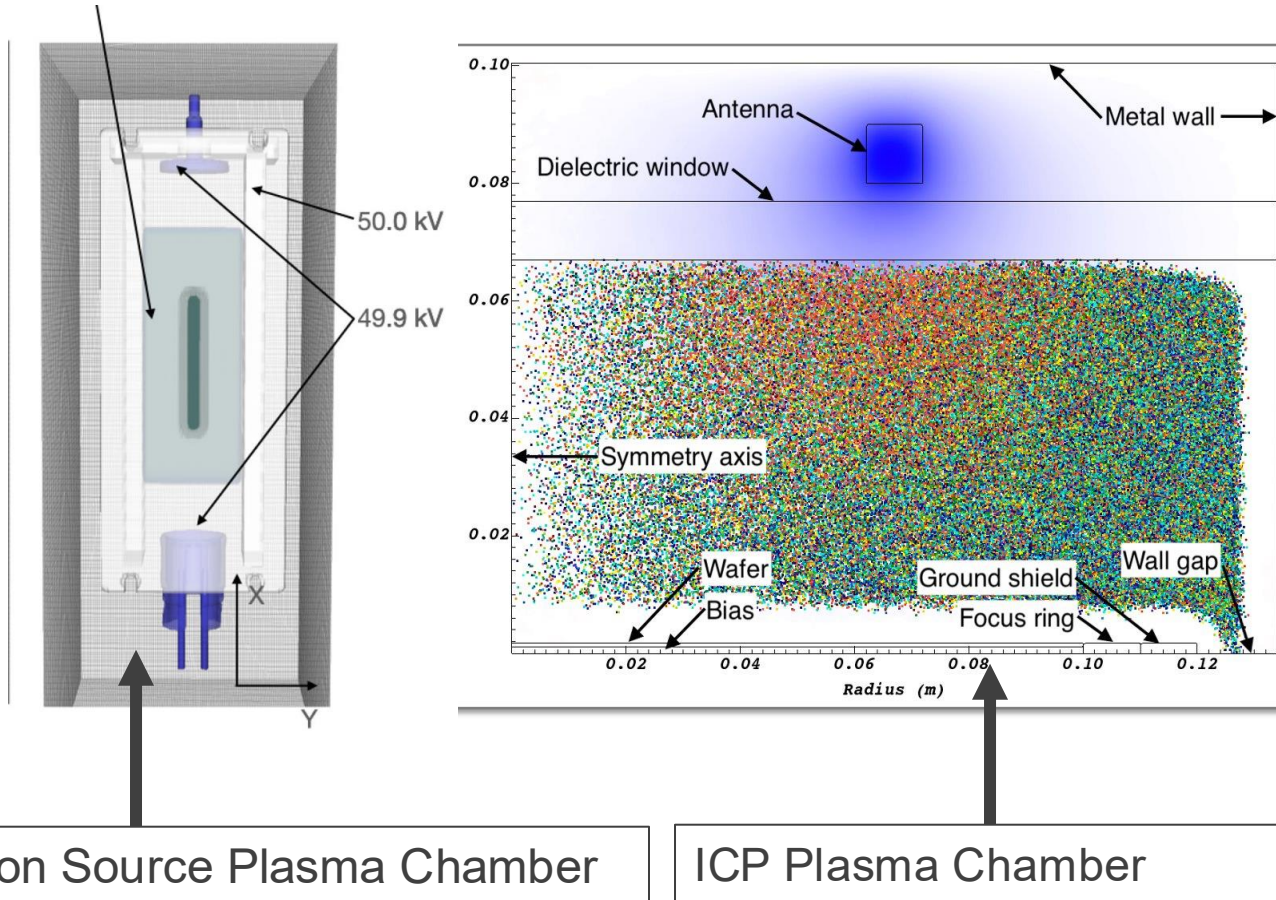
Expanding Market Opportunity Using AI Digital Twin Modeling

Fab Technology Co-Optimization™ brings new era of AI-assisted process optimization



Ion source modeling with VSimPlasma

Plasma chamber modeling is **synergistic** between CCP/ICP for etch/deposition and ion sources



- Similarities

- Kinetic plasma models are required for accuracy
- Self-consistent particle/particle and particle/surface interactions
- Complex plasma chemistry
- Modeling sheath formation is critical
- Plasma chamber dimensions are similar

- Differences

- Ion sources typically feature magnetic fields for plasma confinement, requiring 3-D models
- Ion sources are driven by both RF and DC power
- Electrons typically are heated by cathodes or direct injection in ion sources
- Ion source plasmas are typically somewhat lower temperature and pressure
- Ion sources have an aperture instead of a wafer

The background of the image is a close-up, high-angle shot of a microchip. The chip's intricate circuitry is visible, with various rectangular and circular components. The lighting is dramatic, with a strong red glow on the left side and a blue glow on the right side, creating a bokeh effect with out-of-focus light spots. The text "Thank you. | SILVACO" is overlaid on the left side of the chip.

Thank you. | **SILVACO**

SILVACO

Why Customers Choose Silvaco

- Leading TCAD solution for SiC, GaN Power Devices
- AI-Driven Fab Technology Co-Optimization (FTCO™)
- Production and market proven design IPs for auto/consumer
- Tools for foundation IPs, embedded memories & IO
- Best-in-class solution for Display technology development
- Unique point tools complementing existing IC design flows
- Agility to respond to customers' unmet needs (R&D, Support)



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Seasoned Management Team with Deep Industry Expertise

Four New Additions – CFO, SVP/GM IP, SVP/GM EDA, VP Business Development



Wally Rhines
CEO and Board Member



Chris Zegarelli
Chief Financial Officer



Ian Chen
Chief Revenue Officer



Candace Jackson
SVP, General Counsel and Corporate Secretary



Eric Guichard, Ph.D
SVP and GM of TCAD



Andrew Wright
SVP and GM of IP



Jasvinder Singh
SVP and GM of EDA



John Berg
VP Business Development

